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					M-1007-6C US		08/632,298	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Applicant(s)			
(Use several sheets if necessary)					Mohsen, Amr M.			
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					April 12, 1996		2763	

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 U.S. PATENT & TRADEMARK OFFICE

U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	4,807,183	2/21/89	Kung et al	364	900	RECEIVED 98 MAY 15 PM 2:10 GROUP 2700
	AB	4,963,768	10/16/90	Agrawal et al	307	465	
	AC	4,979,138	12/18/90	Arrathoon	364	713	
	AD	5,144,548	9/1/92	Salandro	364	138	
	AE	5,448,496	9/5/95	Butts et al	364	489	
	AF	5,452,231	9/19/95	Butts et al	364	489	
	AG	5,612,891	3/18/97	Butts et al	364	489	
	AH	5,657,241	8/12/97	Butts et al	364	489	
	AI	5,661,662	8/26/97	Butts et al	364	489	
	AJ						

Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AK							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)	
AL	Barber et al, "A 64 x 17 Non-Blocking Cross Point Switch," 1988 IEEE Int'l Solid-State Circuits Conf., 18 Feb. 1988, pp. 116, 117 and 142.
AM	Colbry et al, "High-Performance Crosspoint Switch Implemented in CMOS on AVP," Procs. IEEE Int'l. Solid-State Circ. Conf., 5 - 8 Oct. 1987, pp. 113 - 116.
AN	Quinnell, "Digital Switches Provide Bus Connections with No Additional Gate Delays," EDN, 18 July 1991, pp. 64.
AO	"Fast Digital Switches Route Bus Signals," EEPN, undated, 1 p.
AP	"Gallium Arsenide 64 x 64 Crosspoint Switch," preliminary data sheet, Vitesse Semiconductor Corporation, Feb. 1991, pp. 1 - 10.
AQ	"Logically Controlled Chip Interconnection Technique," IBM Tech. Discl. Bull., Vol. 32, No. 3B, Aug. 1989, pp. 294 - 299.
AR	"AD75019 16 x 16 Crosspoint Switch Array," data sheet, Analog Devices, 1991, 4 pp.

Examiner	Date Considered <u>6/9/99</u>
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.